

SYNCHRONOUS DATA TRANSMISSION METHOD

Abstract of the Disclosure

The method is for transmitting data between two devices via a clock wire or line and at least one data wire or line. The clock wire is maintained by
5 default on a logic value A, and each device is capable of tying the clock wire to an electric potential representing a logic value B that is the opposite of A. According to the method, both devices tie the clock wire to B when a datum is transmitted, the device to
10 which the datum is sent does not release the clock wire while it has not read the datum, and the device sending the datum maintains the datum on the data wire at least until an instant when the clock wire is released by the device to which the datum is sent. The method is
15 particularly applicable to communication between a microcomputer and a microprocessor.

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